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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10003673	FILING DATE 11/02/2001	CLASS 714	SUBCLASS 720	GAU 2/33 2184	EXAMINER <i>[Signature]</i>
<b>**APPLICANTS:</b> Felber Norbert; Kaeslin Hubert; <i>[Signature]</i> J. Kerveros					
<b>**CONTINUING DATA VERIFIED:</b> <i>[Signature]</i>					
<b>**FOREIGN APPLICATIONS VERIFIED:</b> YES <i>[Signature]</i> 3/18/04 EUROPEAN PATENT OFFICE (EPO) 00124076.1 11/06/2000					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <i>[Signature]</i> 3/18/04		ATTORNEY DOCKET NO PHCH000024	
TITLE : Method for testing integrated circuit					

U.S. DEPT. OF COMM. / PAT. & TM-PTO-436L (Rev. 12-94)

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
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